



35th INTERNATIONAL CONFERENCE ON VLSI DESIGN & 21st INTERNATIONAL CONFERENCE ON EMBEDDED SYSTEMS

Silicon Catalyzing Computing, Communication and Cognitive Convergence

19th - 23rd February 2022 (Virtual Event)

Silicon continues to fuel and enhance the power of every emerging technology. Last few decades saw Silicon enabling the Digital revolution among all the Verticals – Compute, Connectivity, AI/ML and Smart Infrastructure. This decade is starting to witness a complete paradigm shift of “Confluence” of these verticals with Silicon continuing to be the Master piece harnessing the power of each of these verticals in a way that is unimagined thus far.

VLSID Conference brings together Leaders from Industries, Academia, Research bodies, Government and Standard organizations to a common platform. This edition VLSID 2022 brings you a unique opportunity to time travel the future of technologies and to explore the role of Silicon in decades to come!

CALL FOR PAPERS

Full Paper Submission Deadline	Notification for Acceptance	Submission of Camera-ready Paper
26 SEP 21	11 DEC 21	16 JAN 22
22 OCT. 21	18 DEC. 21	23 JAN. 22

Submission & enquiries: www.vlsid.org

Artificial Intelligence, Machine Learning and their Applications

AI Accelerators, Edge Computing, Approximate Computing, Autonomous Intelligence (ADAS), AI ethics

Analog and Mixed Signal Design

Analog Circuits for Various Applications, Data Converters, High Speed Interfaces, Power Management Circuits, Energy Harvesting Circuits & Systems, Circuits & Systems for AI-oriented Applications

Digital Integrated Circuits and Systems

Digital Circuits for Communication, Arithmetic Circuits, System-on-Chip Design, Network-on-Chip Design, Low-power Logic Design

Emerging Technologies and Devices

Quantum Computing, Neuromorphic Computing, Synaptic Devices, CMOS Technology and Devices, New Age Nano-Electronics, MEMS Devices, GaN and SiC Devices

Embedded System Design

IoT Systems, Cyber-Physical Systems, Hardware/Software Co-design, Embedded Software, Embedded Operating Systems

Electronic Design Automation

Verification, Synthesis, Physical Design, Silicon Engineering, Just-in-time Synthesis, AI enabled algorithms, Formal Verification, Optimization

Packaging and Interconnects

On-chip interconnects, 3D packaging, Wafer-level packaging, Interconnect Technologies

Security and Safety

Functional Safety, Privacy, Cryptography, PUFs, TRNGs, Hardware Trojans, Trusted Computing, Network Security, Side-Channel & Fault Analysis and Countermeasures

Sensors Circuits and Systems

Sensor Interfacing, Instrumentation, Biomedical Circuits and Healthcare Systems, Low Noise Circuits, EMI Immune Design, Autocalibration Techniques, Wearable Electronics, Autonomous Sensors Systems

Power Electronics

High Power Circuits, Power Convertors, Power Optimization Techniques, Power Delivery Networks, Power Switches, High Voltage Circuits and Systems, Power Management for High Voltage Applications, Power Amplifiers

RF Circuits and Systems

Transceiver Architectures, Short-Range Communication, IoT/IoE, WPAN, Ultra-Low Power Wireless Designs, Effective Spectrum Utilization, RF Power Amplifiers, RF Energy Harvesting

Test and Reliability

EMIR, Interconnect, Power Integrity-Signal Integrity (PISI), EMI/EMC Compatibility, Built-in Self-Test, Design for Test, Self-X (Awareness, Repair, Test), On-line Test, Fault Tolerance

Architectures

Chip Architecture, Computer Architecture, High Performance Computing, Configurable Computing, (Embedded) FPGA, Memory Subsystems, In Memory Computing Systems