International VLSI Design & Embedded Systems conference is a Premier Global conference with legacy of over three and half decades. This Global Annual technical conference that focusses on latest advancements in VLSI and Embedded Systems, is attended by over 2000 engineers, students & faculty, industry, academia, researchers, bureaucrats and government bodies. Silicon and Electronics today are the vital forces for all aspects of technology, Industry and human life – enabling rapid advances in Computing, Connectivity, Storage and Sensors which have resulted in path breaking developments in the areas such as AI/ML, 5G, Mobility, Cloud, Edge and more. The future is starting to witness a "Confluence" of these technologies with Silicon continuing to be the fulcrum harnessing the power of each in a way that was unimagined so far. VLSID 2022 conference brings a unique opportunity to time travel the future of technologies and explore the role of Silicon in decades to come. The User/Designer Track at VLSID 2022 invites abstract presentation submissions in the following areas.

CALL FOR USER/DESIGNER

Abstract Submission	Acceptance Notification	Final Presentation
4 Jan'22	17 Jan'22	8 Feb' 22

- **AI, ML and Applications**
- **Analog and Mixed Signal Design**
- **Digital Integrated Circuits and Systems**
- **Emerging Technologies and Devices**
- **Embedded System Design**
- **Security and Safety**
- **Sensors Circuits and Systems**

- **Power Electronics**
- **RF Circuits and Systems**
- **Electronic Design Automation**
- **Test and Reliability**
- **Packaging and Interconnects**
- **Architectures**

Submissions

- All presentation submissions to the User/Designer Track should be made electronically via the submit button by 4th January 2022.
- Use provided presentation template only. You can download presentation template from conference website
- Maximum 8 slides can be used (including 1 title slide, 6 content, 1 thank you). All figures, references should be incorporated in this limit. Time limit for presentation will be ~12 minutes.
- Presentation should clearly state the problem statement/novel ideas, proposed solution (results, and applications) of the contribution, silicon results if relevant / available.
- Use speaker notes to provide more details to the reviewer wherever necessary
- Presentation exceeding the page limit or identifying the authors/organization will be rejected without review
- Submissions will undergo a double-blind review
- For selected presentation, authors need to submit recorded video of the presentation. Further details will be shared with the selected authors after acceptance notification.
- For more information, please reach out at vlsid2022.udt@gmail.com