

VLSI meets AI & Quantum for Cyber-Physical Systems

Call for User Design Track Submission

Important Dates

Final date of submission : 15th October, 2023

Notification for Acceptance: 30th November, 2023

Final PPT Submission: 7th December, 2023

Final Presentation : The date will be announced soon

<https://vlsid.org/user-design-track/>

The world of Cyber Physical Systems (CPS) and rapid adoption of this technology has led to the emergence of Industry 4.0 as we know it today. It is not only transforming the industry but our lives as well by connecting the physical world with the cyber components in an unprecedented manner. The convergence of these systems with artificial intelligence technologies is fueling powerful edge and cloud computing solutions. Similarly, quantum science and technologies are bringing new communication, sensing and secure data processing capabilities to CPS. As we are aware, the semiconductor industry remains key to enable these technologies, offering both opportunities and challenges. It is by working on these advanced technologies and challenges, we can collectively solve global problems in the fields of agriculture, energy, climate, transportation, education, and healthcare. VLSID 2024 provides that platform for industry and academia alike to discuss, deliberate and delve into the frontiers of semiconductors that could eventually enable disruptive technologies for the next generation.

The UDT for VLSI2024 provides an unique opportunity especially to industry folks to share cutting edge technologies that engineers are working on with the wide audience present at the conference and garner feedback and further ideas for stimulation. We are aware of the deadline pressures that industry folks are busy with and the UDT is unique in its format of submission which is far more simplified as compared to the full-length paper submission. This track also allows for the submission of novel technologies and ideas that are WIP. The UDT at VLSID 2024 invites abstract presentation submissions in the following areas.

UDT TOPICS

- Artificial Intelligence, Machine Learning and their Applications
- Analog and Mixed Signal Design
- Digital Integrated Circuits and Systems
- Low power Digital Architectures
- Emerging Technologies and Devices
- Embedded System Design
- Electronic Design Automation
- Security and Safety
- Sensors Circuits and Systems
- Power Electronics
- RF Circuits and Systems
- Test and Reliability
- Architectures
- Packaging and Interconnects

Please Note

- All presentation submissions to User Design Track should be made electronically via the below submit button before the due date.
- Use provided presentation template only. You can download presentation template below.
- Maximum 10 slides can be used (including 1 title, 8 content and 1 thank you slide). All figures, references should be incorporated in this limit. The submission should be done in pdf format only.
- Presentation should clearly state the problem statement/novel ideas, proposed solution (results and applications) of the contribution, silicon results if relevant/available.
- Use speaker notes to provide more details to the reviewer wherever necessary.
- Presentation exceeding the page limit or identifying the authors/organization will be rejected without review.
- Submissions will undergo a double-blind review.
- For selected presentation, authors need to submit recorded video of the presentation. Further details will be shared with the selected authors after acceptance notification.

https://softconf.com/n/vlsid2024_user