Call for Tutorial Proposals @ VLSID-2024, Kolkata

The Organizing Committee for VLSID-2024 invites proposals for tutorials to be conducted at the Conference to be held in Kolkata, India during the period **6-7th January 2024**.

Tutorials are an integral and an important part of all the editions of VLSID, where instructors have the opportunity to share their expertise on a specific field/research with fellow researchers and those who wish to discover said field/research. We are soliciting proposals for topics of academic and industrial applications interests. These topics may be established or it may be emerging – and with profound and/or new insights thereto, that is best articulated by means of an oral presentation. Please note this year we will have the tutorial session in off-line mode. We look forward to your presence at Kolkata for VLSID in January, 2024

We encourage tutorial proposals submissions, including but not limited to:

- **Analog/Mixed-Signal/RF Circuits and Processing systems**
- **Advanced Process/Material, Device Design and Modelling**
- **Digital Logic Design**
- **Hardware and Systems Security:**
- **Neuromorphic Circuits and Systems**
- **Low-Power Low-Voltage Design**
- **Neural Networks, Machine and Deep learning**
- **In-memory computing**
- **Photonic Integrated Circuits**
- **Emerging Memory Technologies**

- **Artificial Intelligence (AI)/Machine Learning (ML) Hardware**
- **Test, Verification, and Reliability**
- **VLSI Systems and Applications**
- **Cryogenic Electronics for Quantum Metrology**
- **Emerging Computing and post-CMOS Technologies:**
- **Photonic Integrated Circuits, Wireless systems 5G and beyond, and Optical Communication**

Last Date of Submission of the Proposal: 28th September 2023

Acceptance Notification: 15th October, 2023

Submission site: https://softconf.com/n/vlsid2024_tutorials/