

VLSID-2024:Chip Design Contest

Guidelines

- **Timeline**
 - Submission deadline through SoftConf: 5th November 2023
 - Announcement of results: 25th November 2023
- **Eligibility**
 - Only M. Tech or PhD research projects are allowed. B. Tech and Course projects are not allowed.
 - The team size should not be more than 5 students excluding faculty guides.
- **Project Conditions**
 - Any completed projects submitted should have been completed within the last two years.
 - Completed post layout simulation: Work should be completed within 1 year (completion date of the design with post layout simulations should be from 01 January 2023 onwards.)
 - Completed silicon with measurement results: Work should have completed within last 2 years (completion date of the design with post layout simulations and measurements should be from 01 January 2022 onwards)
 - Fabricated designs with the measurement results will have preference, However, complete design with post-layout simulations will be eligible to participate too.
 - Measurement results and comparison with prior works will have an advantage.
- **Published Papers / Filed Patent Applications**
 - At least 80% of the authors or inventors must be from the participant team in the Chip Design Contest.
 - Provide the complete published paper(s) or DOI to the paper that appeared in the journal or conference proceeding if it is available in public domain.