

2024 37th International Conference on VLSI Design & The 23rd International Conference on Embedded Systems (VLSID)

Camera Ready Paper Submission Instructions for Authors

The following are the instructions to work out your Camera-Ready Paper, including the details for generation of e-Copyright Form (eCF)

Note:

1. All accepted papers including poster presentations will be published in the conference proceedings
2. Full length papers should not exceed SIX(6) pages, including figures, tables and references. Papers exceeding the normal length may be submitted upon payment of over length page charges of 25 USD for each page in excess of six. A maximum of two extra pages above normal six are permitted.

Step 1: Prepare your revised final manuscript

Before submitting the paper, please ensure that

- The reviewer suggestions (if any) are taken care of. **NO** changes other than the reviewer suggestion are acceptable at this stage.
- There are no grammatical, spelling, or content errors
- The figures and tables are of high quality and their content is reader-friendly
- The paper contains **no headers, no footers, and no page numbers**. This information will be added by the conference publication team.
- Do add all author name/affiliation as per IEEE guidelines. **NO** modification / addition in author list is allowed.

Step 2: IEEE-Xplore-compatible PDF generation

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Registration Deadline

Ensure at least one author of each selected paper / poster is registered under Author category on the conference registration portal (including page charges if any). If you have any additional questions, please feel free to get in touch: vlsid24.presentation@gmail.com

The deadline for **Camera Ready Paper Submission and Registration** is **20th November 2023**.