

Tutorial Day 1 | 26 Feb 2022 | Saturday

Start	End	Duration	Track 1	Track 2
08:30	09:00	00:30	Welcome/Registration to VLSID 2022	
09:00	09:30	00:30	Opening Keynote Address Prof. Ramgopal Rao, IIT Delhi	
09:30	10:15	00:45	Brain Inspired Computing: The Extraordinary Voyages from Known to Unknown Worlds Prof. Hai Li (Duke University)	Event Cameras: From Biology to Circuits to Applications
10:15	11:00	00:45		Prof Tobi Delbruck (INI/ETH Zurich/UZH)
11:00	11:45	00:45	SoC Architectures for accelerating AI Computing Bharat Daga (Intel)	Event-driven low-compute bioinspired processing for Edge Audio,
11:45	12:30	00:45		Prof. Shih-Chii Liu (INI/ETH Zurich/UZH)
12:30	13:30	01:00	Lunch Break	
13:30	14:15	00:45	AI for Quantum, quantum for AI Nagendra Nagaraja (Qpi Technology)	On-Chip Memories - Challenges, Opportunities, and Recent Advances
14:15	15:00	00:45		Prof. Manan Suri (IIT-Delhi), Anuj Grover (IIIT-Delhi)
15:00	15:45	00:45	Introduction to Memory technology & Products Kalyan Kavalipurapu and Venkat Bringivijayaraghavan, (Micron Technology)	HBM System and Architecture for AI applications
15:45	16:30	00:45		Manish Jain and Nikhil Raghavendra Rao (Rambus)

Tutorial Day 2 27 Feb 2022 Sunday				
Start	End	Duration	Track 1	Track 2
08:30	09:00	00:30	Welcome/Registration to VLSID 2022	
09:00	09:30	00:30	Keynote Address Prof. Subhasis Chaudhuri, Director, IIT Bombay	
09:30	10:15	00:45	Compound Semiconductor with particular focus on the increasing role of wide bandgap semiconductors in electronics Prof. Srabanti Chowdhury (Stanford)	Beyond 5G & 6G Wireless Communication Tushar Vrind (Samsung)
10:15	11:00	00:45		
11:00	11:45	00:45	Heterogenous Integration (HI) using advanced packaging Arun Chandrasekhar & Rupesh Pothineni (Intel)	Meeting Signal Integrity & Reliability Requirements in Automotive SoC Murali Mohan Thota (Texas Instruments)
11:45	12:30	00:45		
12:30	13:30	01:00	Lunch Break	
13:30	14:15	00:45	Building a multi protocol wireless IoT Device Tarun Chaplot (Silicon Labs)	Security for Deep Learning – A Hardware Perspective Prof. Chip Hong Chang and Wang Si (NTU)
14:15	15:00	00:45		
15:00	15:45	00:45	Logic Design of a RISC-V Microprocessor Rajat Gupta (Sensonics)	AI in Security – A Potential to Make and Break a Secure Connected World Prof. Debdeep Mukhopadhyay (IIT KGP)
15:45	16:30	00:45		

Conference Day 1 | 28 Feb 2022 | Monday

Start	End	Duration	Conference Agenda			
08:30	09:00	00:30	Welcome/Registration to VLSID 2022			
09:00	10:00	01:00	Inauguration Ceremony Sh. Ashwini Vaishnaw (Hon. Minister of Railways & IT), Sh. Rajeesh Chandrasekhar (Hon. Minister of State for Electronics & IT) Sh. Basavaraj Bommai (Hon. Chief Minister, Karnataka), Dr. Ashwath Narayan (Minister of Electronics & IT, Karnataka) Other Government and Industry Dignitaries			
10:00	10:10	00:10	Conference Opening			
10:10	10:40	00:30	Vision Address Nick McKeown, SVP & GM, Network and Edge Group, Intel			
10:40	11:10	00:30	Vision Address H.-S. Philip Wong, Willard R. and Inez Kerr Bell Professor, Stanford			
11:10	12:10	01:00	Panel Discussion Shaping the Future of Semiconductors in India Prof. V. Kamakoti (IIT Madras), Raja Manickam (Tata Electronics), Sagar Sharma (Invest India), Dr. Satya Gupta (VLSI Society, India) Moderator: Nirmal John (Economic Times)			
Start	End	Duration	Track 1 Technical Papers	Track 2 Technical Papers	Track 3 Technical Papers	Track 4 Industry Forum
12:10	13:10	01:00	Session: 1A Analog and Mixed signal	Session: 1B Embedded Systems	Session: 1C Electronics Design Automation	Session: IF-1
13:10	14:00	00:50	Lunch Break			
14:00	14:30	00:30	Keynote Joe Sawicki, Executive Vice President, Siemens Digital Industries Software			
14:30	15:00	00:30	Keynote Shankar Krishnamoorthy, GM, Silicon Realization Group, Corporate Staff, Synopsys			
15:00	16:00	01:00	Session: 2A Analog and Mixed signal	Session: 2B Embedded Systems	Session: 2C Electronics Design Automation	Session: IF-2
16:00	16:10	00:10	Closing Session			

Conference Day 2 01 March 2022 Tuesday						
Start	End	Duration	Conference Agenda			
09:00	09:30	00:30	Vision Address Suk Hwan Lim, Executive Vice President, Head of America S.LSI R&D Center, Samsung			
09:30	10:00	00:30	Vision Address Jason Cong, Volgenau Chair for Engineering Excellence and Director of VAST, UCLA			
Start	End	Duration	Track 1 Technical Papers	Track 2 Technical Papers	Track 3 Technical Papers	Track 4 Industry Forum
10:00	11:40	01:40	Session: 3A Architecture	Session: 3B Security and Safety	Session: 3C Electronics Design Automation	Session: IF-3
11:40	12:10	00:30	Keynote Sanjive Agarwala, Corporate VP and GM, IP Group, Cadence			
12:10	13:10	01:00	Panel Discussion Designing Product Excellence (The Next Edge) Naveed Sherwani (Rapid Silicon), Sunil M (Blue Semi), Sai Mopuri (Analog Devices) Moderator: Jaswinder Ahuja (Cadence)			
13:10	14:00	00:50	Lunch Break			
14:00	14:30	00:30	Keynote Andrew B Kahng, Distinguished Professor of CSE and ECE, UC San Diego			
14:30	16:10	01:40	Session: 4A Selected Papers from DAC	Session: 4B Artificial Intelligence & ML	Session: 4C RF Circuits/Systems and Sensors	Session: IF-4
16:10	17:40	01:30	Awards Session Presided by Sonam Wangchuk, Himalayan Institute of Alternative Ladakh			

Conference Day 3 02 March 2022 Wednesday						
Start	End	Duration	Conference Agenda			
09:00	09:30	00:30	Keynote Ken Wiseman, Vice President Technology, Qualcomm			
09:30	10:00	00:30	Keynote Daisy Chittilapilly, President, CISCO India and SAARC			
Start	End	Duration	Track 1 Technical Papers	Track 2 Technical Papers	Track 3 Student Research Forum	Track 4 User Design Track
10:00	11:40	01:40	Session: 5A Digital Design	Session: 5B Test/Reliability and Power Electronics	Session: 5C SRF Session: SRF-1	User Design Track: UDT-1
11:40	12:10	00:30	Keynote Manish Kothari, VP. Silicon Labs			
12:10	13:10	01:00	Panel Discussion Building Semiconductor Research & Talent Navakant Bhat (IISc), Rituparna Mandal (MediaTek), Sanjay Gupta (NXP Semiconductors), Sunita Verma (MeitY) Moderator: Vivek Sharma (ST Micro Electronics)			
13:10	14:00	00:50	Lunch Break			
14:00	15:00	01:00	Start-up Showcase Product/Technology Demonstration by 5 Most Promising Startups			
15:00	16:40	01:40	Session: 6A Packaging & Emerging Technologies	Session: 6B Emerging Technologies and Devices	RF Session: SRF-2	User Design Track: UDT-2
16:40	16:55	00:15	Closing Session			