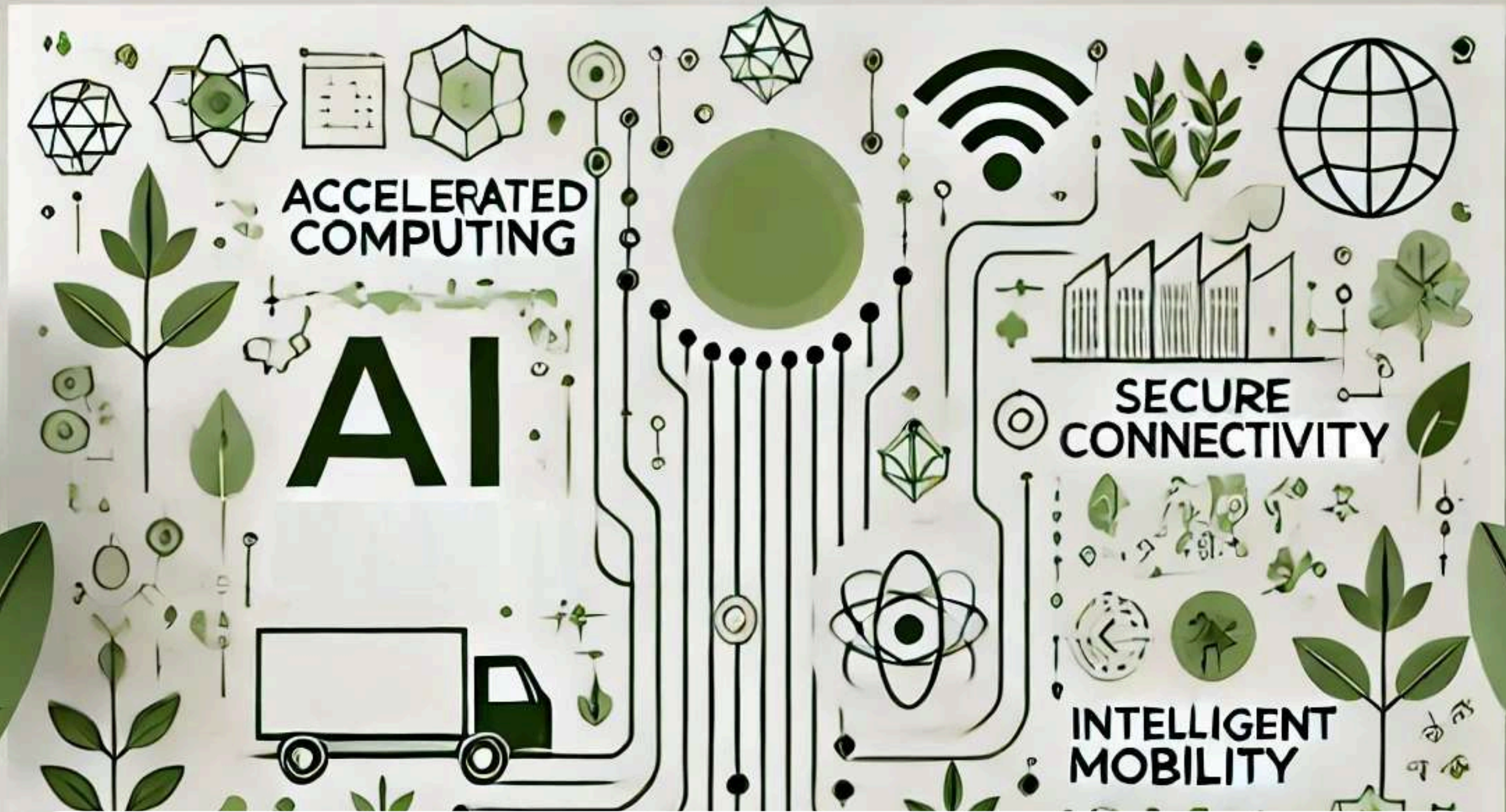


SILICON MEETS AI



Silicon Meets AI: Sustainable Innovations in Accelerated Computing, Secure Connectivity, and Intelligent Mobility

4-8 JANUARY 2025 | BENGALURU

TOPICS

- ✓ Analog / Mixed Signal / RF Circuits & Processing Systems
- ✓ Advanced Process/Material, Device Design and Modelling
- ✓ Digital Logic Design, VLSI Systems and Applications
- ✓ Hardware and Systems Security
- ✓ Neuromorphic Circuits & Systems
- ✓ Low power & Low voltage Design
- ✓ In-Memory Computing
- ✓ Emerging Memory Technologies
- ✓ Artificial Intelligence (AI)/Machine Learning (ML) Hardware
- ✓ Test, Verification and Reliability
- ✓ Cryogenic Electronics for Quantum Metrology
- ✓ Quantum Computing, EDA for Quantum Computing
- ✓ AI for Electronic Design Automation
- ✓ Emerging Computing and post-CMOS Technologies
- ✓ Photonic Integrated Circuits
- ✓ Wireless systems 5G & beyond, and Optical Communication



Submissions: www.vlsid.org
Enquiries: www.vlsid.org/contact-us

Final Date of Submission:

15th Sep 2024

Notification for Acceptance:

15th Oct 2024



CALL FOR TUTORIALS



The Organizing Committee for VLSID-2025 invites proposals for tutorials at the 38th International VLSI Design Conference being held in Bengaluru, India from 4th to 5th January 2025.

Tutorials are an integral and an important part of all the editions of VLSID, where instructors have the opportunity to share their expertise on a specific field/research with fellow researchers and those who wish to discover said field/research. We are soliciting proposals for topics of academic and industrial applications interests. These topics may be established or it may be emerging – and with profound and/or new insights thereto, that is best articulated by means of an oral presentation. Please note this year we will have the tutorial session in off-line mode. We look forward to your presence at Bengaluru for VLSID in January, 2025



PROPOSAL GUIDELINES

- Format of the Tutorial: 90 minutes (one and half hours) tutorials
- Tutorial Title:
- Area of the Tutorial: List out the broad domain (s) the tutorial is covering
- Keywords: Provide 5 keywords
- Learning Objectives: Explain why the proposed topic is important to the VLSI Design 2025 attendees and what participants will learn by attending the tutorial. Provide topics outlining the contents of the tutorial and the learning outcomes.
- Target audience: Enlist the categories of the audience that will get benefit from this tutorial
- Prerequisite knowledge of the audience: It is expected that the tutorial may be a fine mix of basics and well as advanced concepts related to the topic chosen. The audience is expected to consist of UG/PG students as well as from participants from Industry.
- Abstract: Share an abstract that allows conference attendees to quickly and accurately understand the subject and intent of the tutorial. The abstract should not exceed 1000 words.
- Lead Speaker: Maximum one-page professional biographies of the lead tutorial speakers that highlight qualification, experience, and contribution to the proposed subject area of the tutorial.
- List of Speakers: This is in addition to the lead speaker. Some tutorials may feature different speakers that should be advertised, but it is not necessary, nor a requirement. For example, a 90 minutes tutorial could have up to two speakers. If you plan to have multiple speakers, please provide their names, affiliations and short biography and clearly indicate the parts of the tutorial that they will cover.
- Basic Structure of the Tutorial: Outline the time allocated for particular sub-topic(s).

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