



08–12 Jan 2023

Venue – Novotel, HICC, Hyderabad

CALL FOR USER/DESIGNER

Final Date of Submission	Acceptance Notification	Final Presentation
31 Oct. 2022	30 Nov. 2022	10 Jan. 2023

International VLSI Design & Embedded Systems conference is a Premier Global conference with legacy of over three and half decades. This Global Annual technical conference that focusses on latest advancements in VLSI and Embedded Systems, is attended by over 2000 engineers, students & faculty, industry, academia, researchers, bureaucrats and government bodies. Silicon and Electronics today are the vital forces for all aspects of technology, Industry and human life – enabling rapid advances in Computing, Connectivity, Storage and Sensors which have resulted in path breaking developments in the areas such as AI/ML, 5G, Mobility, Cloud, Edge and more. The future is starting to witness a “Confluence” of these technologies with Silicon continuing to be the fulcrum harnessing the power of each in a way that was unimaginable so far. VLSID 2023 conference brings a unique opportunity to time travel the future of technologies and explore the role of Silicon in decades to come. The User/Designer Track at VLSID 2023 invites abstract presentation submissions in the following areas.

Topics

- Artificial Intelligence, Machine Learning and their Applications
- Analog and Mixed Signal Design
- Digital Integrated Circuits and Systems
- Low power Digital Architectures
- Emerging Technologies and Devices
- Embedded System Design
- Electronic Design Automation
- Security and Safety
- Sensors Circuits and Systems
- Power Electronics
- RF Circuits and Systems
- Test and Reliability
- Architectures
- Packaging and Interconnects

Please Note

- All presentation submissions to User Design Track should be made electronically via the below submit button before the due date.
- Use provided presentation template only. You can download presentation template below.
- Maximum 8 slides can be used (including 1 title, 6 content and 1 thank you slide). All figures, references should be incorporated in this limit. The submission should be done in pdf format only.
- Presentation should clearly state the problem statement/novel ideas, proposed solution (results, and applications) of the contribution, silicon results if relevant/available.
- Use speaker notes to provide more details to the reviewer wherever necessary.
- Presentation exceeding the page limit or identifying the authors/organization will be rejected without review.
- Submissions will undergo a double-blind review.
- For selected presentation, authors need to submit recorded video of the presentation. Further details will be shared with the selected authors after acceptance notification.