VLSI DESIGN CONFERENCE

36th International Conference on VLSI Design & 22nd International Conference on Embedded Systems
8-12 Jan, 2023, HICC, Novotel, Hyderabad

Semiconductors Driving Disruptive Innovations in Global Digitalization

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Executive Summary

- Indexed in IEEE Xplore
- 60:40 - Academic: Industry
- 80:20 - National: International
- Total Paper Submissions: 282
- Accepted Papers: 54
- Accepted Posters*: 17 (* Posters are submitted as complete 6-page IEEE Xplore paper)
- 4 Awards
  - Best Paper (A K Chowdhury Best Paper)
  - Best Student Paper (Nripendra Nath Biswas Best Student Paper)
  - Honorable Mention (Naresh Mallipeddy Honorable Mention Award)
  - Best Poster Award

### Technical Co-Sponsorship
ACM SIGDA, IEEE CAS & CEDA

### Other Statistics
- TPC Members: 280
- Track Chairs: 24
- Total Reviews: 922
- Paper Mgmt. System: Softconf
- Plagiarism Check Tool: Turnitin & IEEE Cross Check
- Invited Talks: 11
- Min. Blind Reviews: 3
- Publications: IEEE Conf. Publication (CPS)
Theme: Hardware & Embedded Software Co-Design with Hardware Acceleration in SoC FPGA

HW Platform: Microchip Technology’s RISC-V Based Polar Fire SoC FPGA Icicle Kit

Idea Areas:

✓ Wireless Systems for Smart Villages
✓ Deep Learning & AI/ML Accelerators
✓ Medical Prosthetics, Tumor Detection, ECG, Tissue Classification
✓ Imaging Algorithms

Statistics

✓ 400 manhours for Ideation of Contest Theme & Planning
✓ 250 manhours for Market Outreach Material Preparation
✓ 200 manhours for Review, Final Selections
✓ 350 manhours for technical support to contestants
✓ 10 Accepted out of 75 total submissions
✓ 2 Awards – 1 Runner, 1 Winner
USER DESIGN TRACK

- Presentation submissions on
  - Artificial Intelligence, Machine Learning and their applications
  - Analog & Mixed Signal Design
  - Digital Integrated Circuits & Systems
  - Lower power Digital Architectures
  - Emerging Technologies & Devices
  - Embedded System Design
  - Electronic Design Automation
  - Security & Safety
  - Sensors, Circuits & Systems
  - Power Electronics
  - RF Circuits & Systems
  - Test & Reliability
  - Architectures
  - Packaging and Interconnects

- 80 plus submissions
- 10 presentations
- 2 Awards - 1 Best UDT, 1 Honorable Mention from Academia
✓ Abstract submissions on

  o VLSI Design & Embedded Systems

✓ 45 plus submissions
✓ 9 final presentations
✓ 2 Awards – 1 Runner, 1 Winner
FELLOWSHIPS

✓ 150 Travel Allowance Grants
✓ 172 Free Registrations
✓ 110+ Fellow attendees
✓ Free Boarding & Lodging (non-locals)
✓ 75+ claimed TA grants

INDUSTRY FORUM

✓ Keynotes & Panel Discussions on
  o 5G Opportunities & Challenges
  o 5G Use cases & how to transform businesses and industries
  o Automotive Innovations for Sustainable & Safe Mobility
  o India on E-mobility highway
  o Semiconductor Manufacturing in India
  o Evolution of Indian EDA Marketplace
KEYNOTES

18 Keynote Speakers – National & International

TUTORIALS

✓ 2 days of Tutorials
✓ Industry In-house Infrastructure and Logistics
✓ 3 Tracks
✓ 12 Sessions
✓ 29 Speakers

START-UP FORUM

✓ Pitches from startups in semiconductors & embedded systems products
✓ Startup interaction with investors and mentors
✓ Entries from 18 Startups
✓ Jury from Industry, startup & investor communities
✓ 11 startups selected for the pitching competition
✓ 2 joint winners
✓ 300 attendees
All Other Contributors

- 40+ Organizing Committee Members
- 110+ Volunteers
- 3 In-house MCs
- 22+ Sponsors
- 60+ Exhibits
- 1600+ Delegates

Special Recognitions

- **Lifetime Achievement Award** – Dr. Satya Gupta - for his outstanding contribution to the Growth and Development of Semiconductor Industry in India

- **Special Recognition Award** – Shri Saurabh Gaur, IAS - for his outstanding contribution in formulating and implementing Semiconductor & Electronics Manufacturing Schemes